## AMENDMENTS TO THE CLAIMS

Please replace the claims, including all prior versions, with the listing of claims below.

## LISTING OF CLAIMS:

1. (currently amended) An input circuit for the detection of an interruption in a differential signal feed, comprising:

a pair of input terminals receiving differential data signals; and

two inputs of a data comparator, connected to the input terminals, to generate data, wherein the two input terminals are connected to respective comparators,

the respective comparators have an auxiliary voltage applied on ~~the~~ an input side, and

signals at outputs of the respective comparators are evaluated such that an interruption of at least one ~~of the signal feeds~~ signal feed is detected.

2. (previously presented) The input circuit as claimed in claim 1, wherein the input circuit is arranged in an integrated circuit.

3. (previously presented) The input circuit as claimed in claim 1, wherein the input terminals are connected via a resistor to a terminating potential.

4. (previously presented) The input circuit as claimed in claim 3, wherein there is at least one current source which is supplied to the auxiliary voltages at the resistors.

5. (previously presented) The input circuit as claimed in claim 1, wherein the auxiliary voltage is greater than the maximum input offset of the comparators and smaller than a minimum voltage swing of the data signal.

6. (previously presented) The input circuit as claimed in claim 2, wherein resistors are arranged in the integrated circuit.

7. (previously presented) The input circuit as claimed in claim 2, wherein resistors are arranged outside the integrated circuit.

8. (previously presented) The input circuit as claimed in claim 7, the input circuit having two current sources, each connected to one of the input terminals and to a supply potential, and each of the two current sources impresses a current which is lower than the currents flowing during normal operation or in a test case.

9. (previously presented) The input circuit as claimed in claim 1, wherein the outputs of the comparators are respectively connected to a boundary scan cell of a boundary scan shift register.

10. (previously presented) The input circuit as claimed in claim 1, wherein the input circuit is configured to be switched off.

11. (currently amended) A method for detection of an interruption in a differential signal feed, comprising:

feeding differential data signals to a pair of input terminals; and

generating data at the pair of input terminals which are connected to two inputs of a data comparator, wherein

the pair of input terminals are connected to a respective comparator,

the respective comparators have an auxiliary voltage applied on ~~the~~ an input side, and

~~the~~ signals at ~~the~~ outputs of the respective comparators are evaluated such that an interruption of at least one ~~of the signal feeds~~ signal feed is detected.